

Lecture 3

Performance Analysis

Performance Metrics, Postulates

Ceng505 *Parallel Computing* at October 11, 2010

Performance Analysis

Computational Models

Equal Duration Model

Parallel Computation with
Serial Sections Model

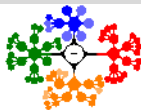
Skeptic Postulates For
Parallel Architectures

Grosch's Law

Amdahl's Law

Gustafson-Barsis's Law

Dr. Cem Özdoğan
Computer Engineering Department
Çankaya University



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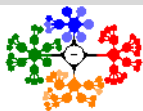
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- Analysis of the performance measures of parallel programs.



Computational Models

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- Analysis of the performance measures of parallel programs.
- Two computational models;



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Equal Duration Model

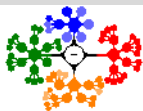
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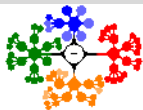
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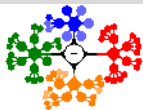
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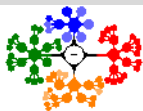
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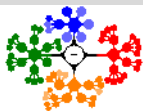
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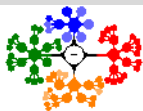
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- The impact of the communication overhead on the overall speed performance of multiprocessors.

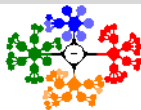


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- The scalability of parallel systems.

Computational Models - Equal Duration Model I

Assume that a given computation can be divided into concurrent tasks for execution on the multiprocessor.

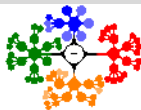
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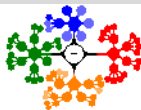
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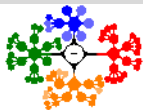


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$$t_p = \frac{t_s}{n}$$



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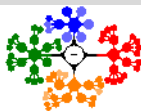
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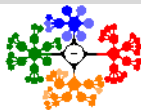
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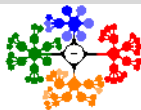
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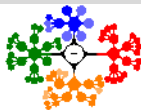
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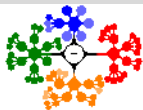
- The speed-up factor of a parallel system can be defined as
 - the ratio between the time taken by a single processor to solve a given problem
 - to the time taken by a parallel system consisting of n processors to solve the same problem.



Computational Models - Equal Duration Model II

- Speed Up;

$$S(n) = \frac{t_s}{t_p} = \frac{t_s}{t_s/n} = n \quad (1)$$

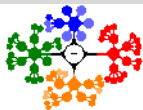


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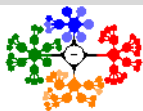


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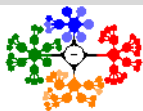
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- Then the actual time taken by each processor to execute its subtask is given by

$$S(n) = \frac{t_s}{t_p} = \frac{t_s}{t_s/n + t_c} = \frac{n}{1 + n * t_c/t_s} \quad (2)$$



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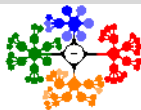
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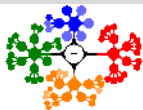
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- This equation indicates that the **relative values of t_s and t_c affect the achieved speed-up factor.**



Computational Models - Equal Duration Model III

- A number of cases can then be studied:



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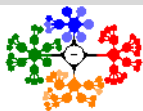
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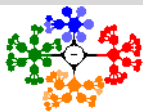
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- A number of cases can then be studied:
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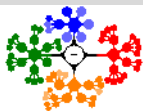
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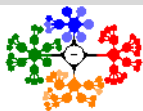
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- The resulting measure is called the efficiency, E .



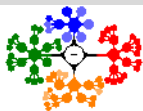
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- The efficiency is a measure of the **speed-up achieved per processor**.



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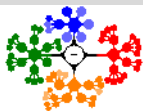
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- The efficiency is a measure of the **speed-up achieved per processor**.
- According to the simple equal duration model, the efficiency E is equal to 1, if the communication overhead is ignored.



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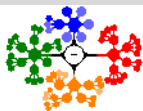
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- The resulting measure is called the efficiency, E .
- The efficiency is a measure of the **speed-up achieved per processor**.
- According to the simple equal duration model, the efficiency E is equal to 1, if the communication overhead is ignored.
- However if the communication overhead is taken into consideration, the efficiency can be expressed as

$$E = \frac{1}{1 + n * t_c/t_s} \quad (3)$$



Computational Models - Equal Duration Model IV

- Although simple, the equal duration model is however unrealistic.



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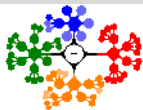
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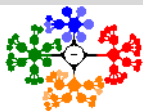
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- These (serial) parts must be executed on a single processor.

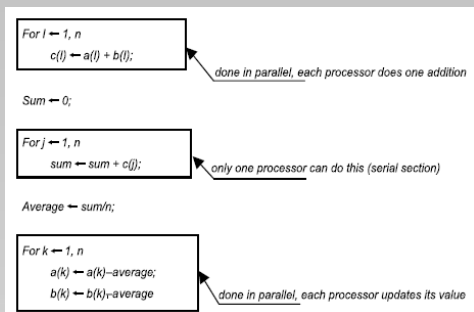
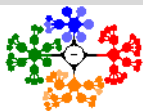
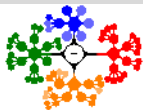


Figure: Example program segments.





- In Fig. 1 program segments, we assume that we start with a value from each of the two arrays (vectors) a and b stored in a processor of the available n processors.

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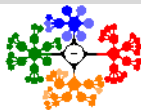
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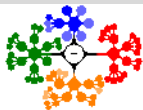
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 - The next program segment cannot be executed in parallel. This block will require that the elements of array c be communicated to one processor and are added up there.

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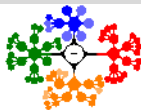
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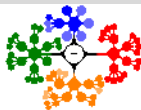
Gustafson-Barsis's Law



- In Fig. 1 program segments, we assume that we start with a value from each of the two arrays (vectors) a and b stored in a processor of the available n processors.
 - The first program block can be done in parallel; that is, each processor can compute an element from the array (vector) c . The elements of array c are now distributed among processors, and each processor has an element.
 - The next program segment cannot be executed in parallel. This block will require that the elements of array c be communicated to one processor and are added up there.
 - The last program segment can be done in parallel. Each processor can update its elements of a and b .

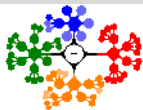
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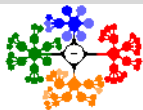
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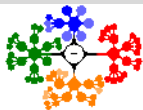
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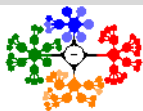
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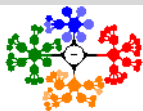
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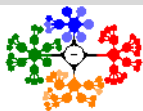
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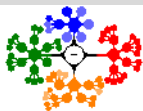
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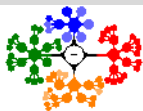


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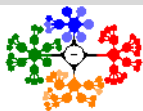


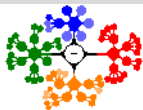
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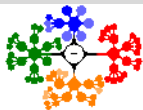




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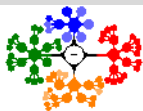
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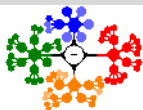
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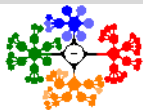
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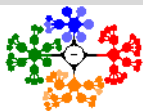
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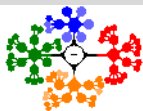
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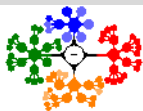
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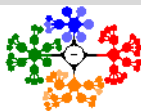
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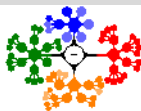
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- As the number of processors increases, it may become difficult to use those processors efficiently.



Postulates - Grosch's Law I

A number of postulates were introduced by some well-known computer architects expressing about the usefulness of parallel architectures.

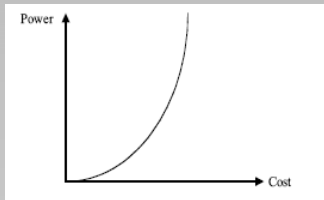
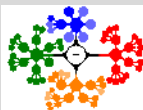


Figure: Power-cost relationship according to Grosch's law.

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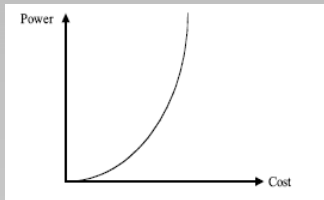
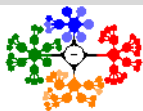


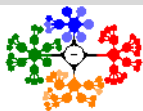
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- He postulated that $P = K * C^s$, where s and K are positive constants. Grosch postulated further that the value of s would be close to 2.



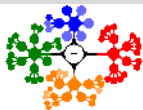
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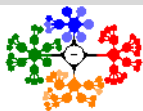
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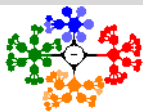
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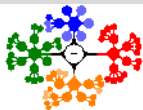
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Performance Analysis

Computational Models

Equal Duration Model

Parallel Computation with
Serial Sections Model

Skeptic Postulates For
Parallel Architectures

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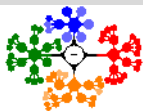
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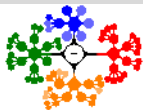
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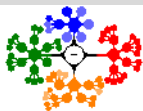
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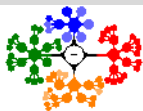


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- This is clearly in contradiction to Amdahl's law.



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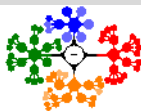
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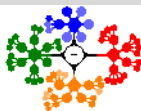
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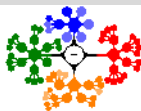
Postulates - Amdahl's Law II

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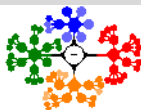
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- Assume further that the solution of such a problem is performed on a binary tree architecture consisting of n nodes (processors).
- Initially, the root node stores the vector $X(m)$ and the matrix $A(m \times m)$ is distributed row-wise among the n processors such that the maximum number of rows in any processor is $m/n + 1$.



Postulates - Amdahl's Law III

A simple algorithm to perform such computation consists of the following three steps:

- 1 The root node sends the vector $X(m)$ to all processors in the order of $O(m * \log n)$



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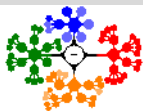
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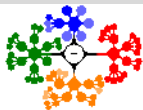
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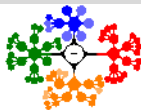
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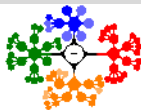
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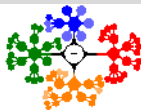
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- Therefore, the fraction of computation that is indivisible

$$f(m) = \frac{(O(m) + O(m * \log n))}{O(m^2)} = O\left(\frac{(1 + \log n)}{m}\right)$$



Postulates - Amdahl's Law IV

- Notice that $\lim_{m \rightarrow \infty} f(m) = 0$.



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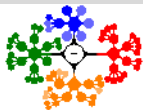
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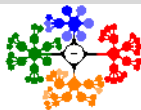
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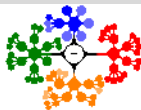


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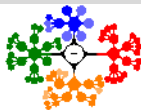
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- The Gustafson-Barsis law makes use of this argument.



Postulates - Gustafson-Barsis's Law I

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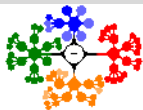
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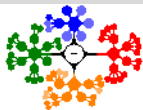
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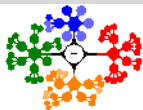
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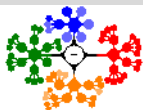
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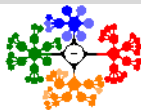
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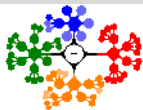
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- They found that to a first approximation the parallel part of the program, not the serial part, scales up with the problem size.



Postulates - Gustafson-Barsis's Law II

- They postulated that if s and p represent respectively the serial and the parallel time spent on a parallel system,



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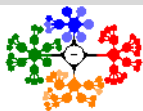
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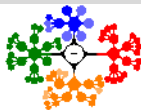


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- Speed-up should be measured by scaling the problem to the number of processors, not by fixing the problem size.

