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Performance Analysis Computational Models

Equal Duration Model
Parallel Computation with
Serial Sections Model
Skeptic Postulates For
Parallel Architectures
Grosch's Law
Amdahi's Law
Gustafson-Barsis's Law

Lecture 3
Performance Analysis

Performance Metrics, Postulates

Ceng471 Parallel Computing at October 14, 2010

Dr. Cem Özdoğan Computer Engineering Department Çankaya University

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Amdahl's Law

Gustafson-Barsis's Law

Analysis of the <u>performance measures</u> of parallel programs.

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- Analysis of the <u>performance measures</u> of parallel programs.
- Two computational models;

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- Analysis of the <u>performance measures</u> of parallel programs.
- Two computational models;
 - 1 the equal duration processes

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- Analysis of the <u>performance measures</u> of parallel programs.
- Two computational models;
 - 1 the equal duration processes
 - 2 parallel computation with serial sections.

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- Analysis of the <u>performance measures</u> of parallel programs.
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Skeptic Postulates For Parallel Architectures

- Analysis of the performance measures of parallel programs.
- Two computational models;
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 - 2 parallel computation with serial sections.
- Two measures;
 - 1 speed-up factor
 - 2 efficiency.

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Guetafenn-Rareie'e Law

- Equal Duration Model
- Skeptic Postulates For Parallel Architectures
- Amdahl's Law

- Analysis of the performance measures of parallel programs.
- Two computational models:
 - 1 the equal duration processes
 - 2 parallel computation with serial sections.
- Two measures;
 - speed-up factor
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- The impact of the communication overhead on the overall speed performance of multiprocessors.

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- Analysis of the <u>performance measures</u> of parallel programs.
- Two computational models;
 - 1 the equal duration processes
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- Two measures:
 - 1 speed-up factor
 - efficiency.
- The impact of the communication <u>overhead</u> on the overall speed performance of multiprocessors.
- The <u>scalability</u> of parallel systems.

Assume that a given computation <u>can be divided</u> into <u>concurrent tasks</u> for execution on the multiprocessor.

 In this model (t_s: execution time of the whole task using a single processor),

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Equal Duration Model

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$$t_p = \frac{t_s}{n}$$

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Equal Duration Model

Equal Duration Model Parallel Computation with

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Equal Duration Model

Parallel Computation with

Assume that a given computation <u>can be divided</u> into concurrent tasks for execution on the multiprocessor.

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$$t_p=\frac{t_s}{n}$$

 since all processors are executing their subtasks simultaneously, then the time taken to execute the whole task is

$$t_p = \frac{t_s}{p}$$

- The speed-up factor of a parallel system can be defined as
 - the ratio between the time taken by a <u>single processor</u> to solve a given problem
 - to the time taken by a parallel system consisting of n processors to solve the same problem.

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Equal Duration Model

· Speed Up;

$$S(n) = \frac{t_{s}}{t_{p}} = \frac{t_{s}}{t_{s}/n} = n \tag{1}$$

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Computational Model

Equal Duration Model

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 This equation indicates that, according to the equal duration model, the speed-up factor resulting from using n processors is equal to the number of processors used (n). **Performance Analysis**

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Equal Duration Model

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- One important factor has been ignored in the above derivation.

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Performance Analysis Computational Models

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- This equation indicates that, according to the equal duration model, the speed-up factor resulting from using n processors is equal to the number of processors used (n).
- One important factor has been ignored in the above derivation.
- This factor is the <u>communication overhead</u>, t_c, which results from the time needed for processors to <u>communicate</u> and possibly <u>exchange data</u> while executing their subtasks.

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- One important factor has been ignored in the above derivation.
- This factor is the communication overhead, t_c, which results from the time needed for processors to communicate and possibly exchange data while executing their subtasks.
- Then the <u>actual time</u> taken by each processor to execute its subtask is given by

$$S(n) = \frac{t_{s}}{t_{p}} = \frac{t_{s}}{t_{s}/n + t_{c}} = \frac{n}{1 + n * t_{c}/t_{s}}$$
(2)

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Equal Duration Model Parallel Computation with

Speed Up;

$$S(n) = \frac{t_s}{t_p} = \frac{t_s}{t_s/n} = n \tag{1}$$

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 This equation indicates that the relative values of t_s and t_c affect the achieved speed-up factor. Performance Analysis

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Equal Duration Models

A number of cases can then be studied:

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Equal Duration Model

- A number of cases can then be studied:
 - 1 if $t_{\rm c} \ll t_{\rm s}$ then the potential speed-up factor is approximately n

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Equal Duration Model

- A number of cases can then be studied:
 - 1 if $t_{\rm c} \ll t_{\rm s}$ then the potential speed-up factor is approximately n
 - 2 if $t_c\gg t_s$ then the potential speed-up factor is $t_s/t_c\ll 1$

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- A number of cases can then be studied:
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 - 3 if $t_c = t_s$ then the potential speed-up factor is $n/n + 1 \cong 1$, for $n \gg 1$.

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- In order to scale the speed-up factor to a value between 0 and 1, we divide it by the number of processors, *n*.

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- In order to scale the speed-up factor to a value between 0 and 1, we divide it by the number of processors, n.
- The resulting measure is called the efficiency, E.

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Computational Models Equal Duration Model

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- In order to scale the speed-up factor to a value between 0 and 1, we divide it by the number of processors, n.
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- The efficiency is a measure of the speed-up achieved per processor.

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Performance Analysis Computational Models

Equal Duration Model Parallel Computation with

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- The efficiency is a measure of the speed-up achieved per processor.
- According to the simple equal duration model, the efficiency E is equal to 1, if the communication overhead is ignored.

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Equal Duration Model Parallel Computation with

- 1 if $t_{\rm c} \ll t_{\rm s}$ then the potential speed-up factor is approximately n
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- 3 if $t_c = t_s$ then the potential speed-up factor is $n/n + 1 \cong 1$, for $n \gg 1$.
- In order to scale the speed-up factor to a value between 0 and 1, we divide it by the number of processors, n.
- The resulting measure is called the efficiency, E.
- The efficiency is a measure of the speed-up achieved per processor.
- According to the simple equal duration model, the efficiency E is equal to 1, if the communication overhead is ignored.
- However if the communication overhead is taken into consideration, the efficiency can be expressed as

$$E = \frac{1}{1 + n * t_c/t_s} \tag{3}$$

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Equal Duration Model Parallel Computation with

 Although simple, the equal duration model is however unrealistic.

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Equal Duration Model

- Although simple, the equal duration model is however unrealistic.
- This is because it is based on the <u>assumption</u> that a given task can be divided into a number of equal subtasks.

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Equal Duration Model

- Although simple, the equal duration model is however unrealistic.
- This is because it is based on the <u>assumption</u> that a given task can be divided into a number of equal subtasks.
- However, real algorithms contain <u>some</u> (serial) parts that cannot be divided among processors.

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Computational Model

Equal Duration Model

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- Although simple, the equal duration model is however unrealistic.
- This is because it is based on the <u>assumption</u> that a given task can be divided into a number of equal subtasks.
- However, real algorithms contain some (serial) parts that cannot be divided among processors.
- These (serial) parts must be executed on a single processor.

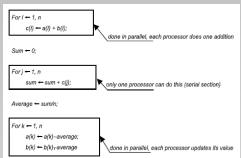


Figure: Example program segments.



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 In Fig. 1 program segments, we assume that we start with a value from each of the two arrays (vectors) a and b stored in a processor of the available n processors.

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- In Fig. 1 program segments, we assume that we start with a value from each of the two arrays (vectors) a and b stored in a processor of the available n processors.
 - The first program block can be done in parallel; that is, each processor can compute an element from the array (vector)
 c. The elements of array c are now distributed among processors, and each processor has an element.

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 c. The elements of array c are now distributed among processors, and each processor has an element.
 - The next program segment cannot be executed in parallel.
 This block will require that the elements of array c be communicated to one processor and are added up there.
 - The last program segment can be done in parallel. Each processor can update its elements of *a* and *b*.

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Computational Wode

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 It is assumed (or known) that a fraction f of the given task (computation) is not dividable into concurrent subtasks.

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Computational Models Equal Duration Model

Parallel Computation with Serial Sections Model

- It is assumed (or known) that **a fraction** *f* of the given task (computation) is <u>not dividable</u> into concurrent subtasks.
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- The time required to execute the task on *n* processors is

$$t_p = t_s * f + (1 - f) * (t_s/n)$$

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• The speed-up factor is therefore given by

$$S(n) = \frac{t_s}{t_s * f + (1 - f) * (t_s/n)} = \frac{n}{1 + (n - 1) * f}$$
 (4)

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 According to this equation, the <u>potential speed-up</u> due to the use of *n* processors is determined primarily by the fraction of code that cannot be divided.

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- According to this equation, the <u>potential speed-up</u> due to the use of *n* processors is determined primarily by the fraction of code that cannot be divided.
- If the task (program) is completely serial, that is, f = 1, then no speed-up can be achieved regardless of the number of processors used.

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Parallel Computation with Serial Sections Model

• This principle is known as Amdahl's law.

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Computational Models Equal Duration Model

Parallel Computation with Serial Sections Model

- This principle is known as <u>Amdahl's law</u>.
- It is interesting to note that according to this law, the maximum speed-up factor is given by

$$lim_{n\to\infty}S(n)=\frac{1}{f}$$

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 Therefore, the improvement in performance (speed) of a parallel algorithm over a sequential one is **Performance Analysis**

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- Therefore, the improvement in performance (speed) of a parallel algorithm over a sequential one is
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- Therefore, the improvement in performance (speed) of a parallel algorithm over a sequential one is
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 - but rather
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- According to Amdahl's law, researchers were led to believe that a substantial increase in speed-up factor would **not be possible** by using parallel architectures.

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Parallel Computation with Serial Sections Model

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- NOT parallelizable;

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$$lim_{n\to\infty}S(n)=\frac{1}{f}$$

- Therefore, the improvement in performance (speed) of a parallel algorithm over a sequential one is
 - limited not by the number of processors employed
 - but rather
 by the fraction of the algorithm that cannot be parallelized.
- According to Amdahl's law, researchers were led to believe that a substantial increase in speed-up factor would **not be possible** by using parallel architectures.
- NOT parallelizable;
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The maximum speed-up factor under such conditions is given by

$$S(n) = \frac{t_{s}}{t_{s} * f + (1 - f) * (t_{s}/n) + t_{c}} = \frac{n}{(n - 1) * f + 1 + n * (t_{c}/t_{s})}$$

$$\lim_{n \to \infty} S(n) = \lim_{n \to \infty} \frac{n}{(n - 1) * f + 1 + n * (t_{c}/t_{s})} = \frac{1}{f + (t_{c}/t_{s})}$$

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$$E(no\ communication\ overhead) = \frac{1}{1+(n-1)*f}$$

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 As the number of processors increases, it may become difficult to use those processors efficiently. Performance Analysis

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Postulates - Grosch's Law I

A number of postulates were introduced by some well-known computer architects expressing about the <u>usefulness of parallel architectures</u>.

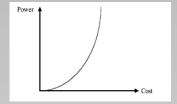


Figure: Power-cost relationship according to Grosch's law.

 It was as early as the late 1940s that H. Grosch studied the relationship between the power of a computer system, P, and its cost, C.



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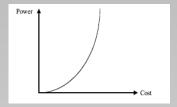


Figure: Power-cost relationship according to Grosch's law.

- It was as early as the late 1940s that H. Grosch studied the relationship between the power of a computer system, P, and its cost, C.
- He postulated that $P = K * C^s$, where s and K are positive constants. Grosch postulated further that the value of s would be close to 2.

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Amdahl's Law Guetafeon, Rareie's Law



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Grosch's Law Amdahl's Law

Gustafson, Rarsis's Law

 Simply stated, Grosch's law implies that the power of a computer system increases in proportion to the square of its cost (see Fig. 6).

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- Simply stated, Grosch's law implies that the power of a computer system increases in proportion to the square of its cost (see Fig. 6).
- Alternatively, one can express the cost of a system as
 C = sqrt(P/K) assuming that s = 2.

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- Alternatively, to do a computation twice as cheaply, one has to do it four times slower.
- With the advances in computing, it is easy to see that Grosch's law is overturned, and it is possible to build faster and less expensive computers over time.

Postulates - Amdahl's Law I

 Similar to Grosch's law, Amdahl's law made it so pessimistic to build parallel computer systems. **Performance Analysis**

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 Due to the intrinsic limit set on the performance improvement (speed) regardless of the number of processors used.



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- However, it has been practically observed that some real parallel algorithms have a fraction that is a <u>function of n</u>.
- Let us assume that f is a function of n such that $\lim_{n\to\infty} f(n) = 0$

$$lim_{n\to\infty}S(n)=lim_{n\to\infty}\frac{n}{1+(n-1)*f(n)}=n \qquad (7)$$



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$$lim_{n\to\infty}S(n)=lim_{n\to\infty}\frac{n}{1+(n-1)*f(n)}=n \qquad (7)$$

• This is clearly in contradiction to Amdahl's law.

 It is therefore possible to achieve a linear speed-up factor for large-sized problems, given that

$$\lim_{n\to\infty} f(n) = 0$$

a condition that has been practically observed.

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 For example, researchers at the Sandia National Laboratories have shown that using a 1024-processor hypercube multiprocessor system for a number of engineering problems, a linear speed-up factor can be achieved. **Performance Analysis**

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- Assume further that the solution of such a problem is performed on a binary tree architecture consisting of n nodes (processors).
- Initially, the root node stores the vector X(m) and the matrix $A(m \times m)$ is distributed row-wise among the n processors such that the maximum number of rows in any processor is m/n+1.

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A simple algorithm to perform such computation consists of the following three steps:

1 The root node sends the vector X(m) to all processors in the order of O(m * log n)

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$$O(m*(m/n+1)) = O(m) + O(\frac{m^2}{n})$$

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- 3 All processors send their C_i values to the root node in O(m * logn).
- According to the above algorithm, the amount of computation needed is

$$O(m*logn) + O(m) + O(\frac{m^2}{n}) + O(m*logn) = O(m^2)$$

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• Therefore, the fraction of computation that is indivisible

$$f(m) = \frac{(O(m) + O(m * logn))}{O(m^2)} = O(\frac{(1 + logn)}{m})$$

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• Notice that $\lim_{m\to\infty} f(m) = 0$.

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- Naturally, this argument is more applicable to message passing parallel architectures than it is to shared memory ones.

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- This assumption amounts to us saying that with *n* processors, the memory is *n* times larger.
- Naturally, this argument is more applicable to message passing parallel architectures than it is to shared memory ones.
- The Gustafson-Barsis law makes use of this argument.

 In 1988, Gustafson and Barsis at Sandia Laboratories studied the paradox created by Amdahl's law.

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- In introducing their law, Gustafson recognised that the fraction of indivisible tasks in a given algorithm might not be known a <u>priori</u>.

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- Recall that Amdahl's law assumes that the amount of time spent on the parts of the program that can be done in parallel, (1 f), is independent of the number of processors, n.

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- In introducing their law, Gustafson recognised that the fraction of indivisible tasks in a given algorithm might not be known a priori.
- They argued that in practice, the problem size scales with the number of processors, *n*.
- Recall that Amdahl's law assumes that the amount of time spent on the parts of the program that can be done in parallel, (1 f), is independent of the number of processors, n.
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Performance Analysis

Computational Models
Equal Duration Model
Parallel Computation with
Serial Sections Model
Skeptic Postulates For
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Grosch's Law
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- They found that to a first approximation the parallel part of the program, not the serial part, scales up with the problem size.

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 They postulated that if s and p represent respectively the serial and the parallel time spent on a parallel system, **Performance Analysis**

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Performance Analysis

Computational Models Equal Duration Model Parallel Computation with Serial Sections Model Skeptic Postulates For Parallel Architectures Grosch's Law

- - They postulated that if s and p represent respectively the serial and the parallel time spent on a parallel system,
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- They therefore, introduced a new factor, called the scaled speed-up factor, SS(n), which can be computed as:

$$SS(n) = \frac{s + p * n}{s + p} = s + p * n = s + (1 - s) * n = n + (1 - n) * s$$
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Performance Analysis Computational Models

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Parallel Architectures

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Skeptic Postulates For Parallel Architectures

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- This shows clearly that it is possible, even easier, to achieve efficient parallel performance than is implied by Amdahl's speed-up formula.
- Speed-up should be measured by scaling the problem to the number of processors, not by fixing the problem size.